

AGENDA

Thursday July 1, 2004

7:30	Continental Breakfast	
8:00	Introduction, Membership Review	Joe Watts
8:15	Treasurer's Report	Robert Lomenick
8:30	BSIM3/4 Issues	Keith Green, Jane Xi
10:00	Break	
10:15	BSIMSOI Requested Enhancements	Joe Watts
10:30	Standard Resistor Model	Colin McAndrews
10:50	Standard Power MOSFET Model	Keith Green
11:10	Well Proximity Effect	Pei Yao
12:00	Lunch	
1:00	Bipolar Support	Joe Watts
	MEXTRAM Status	Paul Humphries
	HICUM Status	Dave Sheridan
2:00	Verilog-A Update	Colin McAndrews
2:15	Status of Standard Model Implementation	EDA Vendor Members*
2:30	Previous Action Items	Joe Watts
2:45	Membership Matters	All
3:00	Action Item Review	Keith Green
3:15	Next Meeting Planning	All
3:30	Adjourn	

* EDA Vendor Members – Either come prepared to present availability of CMC standard models in your tools or e-mail the information to Joe Watts by June 25.