

# Support of HICUM/L0





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# Support of HICUM Level 0 [1]

## **Strong request to have more support of the new HICUM/L0 model**

-  Model documentation and source code not available on HICUM website.
-  EDA support for HICUM/L0 model implementation not fund by the CMC.

## **HICUM/L0 combines the simplicity of SPICE Gummel-Poon (SGP) model with the major features of HICUM Level 2**

-  Less CPU time and better convergence in comparison with HICUM/L2
-  More accurate simulation results than with SGP

## **Possibility to use a part of the *surplus* to better fund and support this model**

[1] M. Schröter et al., *A Computational Efficient Physics-Based Compact Bipolar Transistor Model for Circuit Design - Part I and II*, IEEE TED, pp. 279-295, February 2006.