

HICUM - Update June 05

- Level2/Version 2.2
 - Verilog implementation has been improved/optimized further to work properly with ADMS (in cooperation with Geoffrey Coram, Colin McAndrew and Laurent Lemaitre)
 - C-code can be generated via ADMS
 - significant time spent on supporting simulator implementation of new version
 - investigation of how to implement:
 - correlated noise (implementation yet to be tested on experimental data)
 - vertical NQS effects
- Level2/Version 2.1
 - still supporting model deployment and bug fixes in certain simulators
- Level0/Version 1.1
 - started to improve Verilog implementation according to Level2
- 5th HICUM Workshop
 - held on June 6/7 at ST in Crolles/Grenoble => www.iee.et.tu-dresden.de/iee/eb/eb_homee.html