

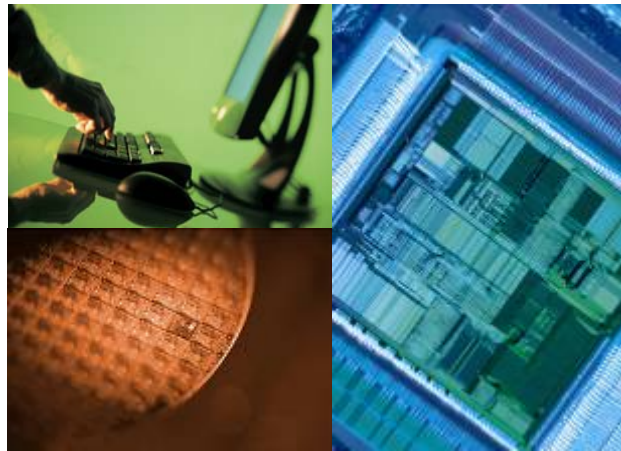
Synopsys CMC Standard Model Status and Plan

Weidong Liu

Analog Mixed Signal Group

Synopsys, Inc.

December 15, 2006



HSPICE

Models	Latest HSPICE Release (2006.09-SP1; December 2006)	Future HSPICE Release (2007.03; March 2007)
BSIM3	3.3.0	3.3.0
BSIM4	4.5.0	4.6.0
BSIMSOI	3.2/4.0	3.2/4.0
PSP	102.1	102.1
Mextram	504.6	504.6
HICUM	2.21	2.22
HICUM L0	1.11	1.11
JUNCAP	200.1	200.2
MM11	1102.3	1102.3
HiSIM	2.3.0	2.3.1
Verilog-A	2.2	2.2

Disclaimer: The actual delivery in the future release might be different from the plan.

HSIMplus

Models	Latest HSIMplus Release (2006.06-SP2; December 2006)	Future HSIMplus Release (2007.03; March 2007)
BSIM3	3.3.0	3.3.0
BSIM4	4.5.0	4.6.0
BSIMSOI	3.2/4.0	3.2/4.0
PSP	102.0	102.0
Mextram	504.6	504.6
HICUM	2.2	2.21
HICUM L0	1.1	1.11
JUNCAP	200.1	200.1
MM11	1102.1	1102.3
HiSIM	2.3.0	2.3.0
Verilog-A	2.1	2.1

Disclaimer: The actual delivery in the future release might be different from the plan.

NanoSim

Models	Latest NanoSim Release (2006.06-SP2; December 2006)	Future NanoSim Release (2007.03; March 2007)
BSIM3	3.3.0	3.3.0
BSIM4	4.5.0	4.6.0
BSIMSOI	3.2/4.0	3.2/4.0
PSP	102.0	102.0
Mextram	504.6	504.6
HICUM	2.2	2.21
HICUM L0	1.1	1.11
JUNCAP	200.1	200.2
MM11	1102.1	1102.3
Verilog-A	2.2	2.2

Disclaimer: The actual delivery in the future release might be different from the plan.